

Express Mail No. EV139498831US  
Date of Deposit: July 1, 2002



Attorney Docket No. 19282-026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jon C.R. Bennett  
Serial No.: 09/845,541  
Filing Date: April 30, 2001  
Entitled: PACKETIZED DATA DISCARD  
Examiner: Not Yet Assigned  
Art Unit: 2661

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Technology Center 2600

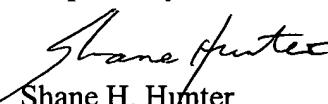
Commissioner for Patents and Trademarks  
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT FILED PURSUANT TO  
THE DUTY OF DISCLOSURE UNDER 37 C.F.R. §§1.56, 1.97 and 1.98

Sir:

Enclosed for the Examiner's review is a replacement for the original article submitted with the Information Disclosure Statement filed June 3, 2002. The article is highlighted on the attached copy of the original 1449 form.

Respectfully submitted,

  
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Registration No. 41,858  
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Date: July 1, 2002

TRA 1675035v1



Please type a plus sign (+) in this box

PTO/SB (12-97)

Approved for use through 9/30/00. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Modified Form 1449/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Serial Number	09/845,541				
		Filing Date	April 30, 2001				
		First Named Inventor	Jon C.R. Bennett				
		Group Art Unit	2661				
		Examiner Name	Not Yet Assigned				
		Attorney Docket Number	19282-026				
<b>U.S. PATENT DOCUMENTS</b>							
Exam Initials	Cite No.	U.S. Patent Document No.	Issue Date	Name of Patentee(s) or Applicant(s)	Class	Sub Class	Filing Date If Appropriate

<b>FOREIGN PATENT DOCUMENTS</b>					
Exam Initials	Cite No.	Foreign Patent Document Office Number	Name of Patentee(s) or Applicant(s)	Date of Publication	Translation Yes No

<b>OTHER NON PATENT LITERATURE DOCUMENTS</b>					
Exam Initials	Cite No.	Name of Author, Title (when appropriate), Publication, Volume, Page(s), Date, Etc.			
		Suter et al, "Design Considerations For Supporting TCP With Per-Flow Queueing", Proceedings of the IEEE Infocom '98, The Conference On Computer Communications 7 <sup>th</sup> Annual Joint Conference of the IEEE Computer and Communications Society, Gateway to the 21 <sup>st</sup> Century, San Francisco, CA , March 29 - April 2, 1998, Proceedings of IEEE I, vol. 1, 29 March 1998, pages 299-306, XP000854205, ISBN: 0-7803-4384-0, Section II.B.			
		LIN Y-S et al., "VLSI Design of a Priority Arbitrator for Shared Buffer ATM Switches", ISCAS '97, Proceedings of 1997 IEEE International Symposium on Circuits and Systems, Circuits and Systems in the Information Age, Hong-Kong, June 9-12, 1997, IEEE International Symposium on Circuits and Systems, New-York, N.Y. IEEE, US, vol. 4, 9 June 1997, pages 2785-2788, XP000805117, ISBN: 7803-3584-8, Sections 3, 4.			
		Notification of Transmittal of The International Search Report or the Declaration, mailed 01/03/02.			

\* a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_\_\_\_, filed \_\_\_\_\_, and relied upon for an earlier filing date under 35 U.S.C. §120 (continuation, continuation-in-part, and divisional applications).

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.

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TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application are the following documents:

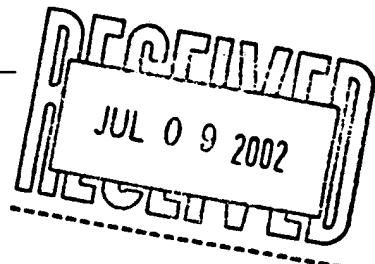
1. Supplemental Information Disclosure Statement Filed Pursuant to The Duty of Disclosure Under 37 C.F.R. §§1.56, 1.97 and 1.98;
2. PTO 1449 form with Supplemental Reference attached as listed; and
3. Return Postcard.

In connection with the foregoing matter, please charge any fees that may be due, to Deposit Account Number 50-0311, Ref. No. 19282-026. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

  
Shane H. Hunter  
Registration No. 41,858  
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Date: July 1, 2002



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